



# Vivekanand Education Society's Institute of Technology

(Affiliated to University of Mumbai, Approved by AICTE & Recognized by Govt. of Maharashtra)

## 3.2- Innovation Ecosystem

**3.2.1: Institution has created an ecosystem for innovations including Incubation centre and other initiatives for creation and transfer of knowledge**

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**Dr. (Mrs.) J. M. Nair**  
M. Tech., Ph.D. (IT & I)  
Principal


Ref. No.: VESIT/ JMN/1325/2023-24

Date: 26/10/2023

## TO WHOM SO IT MAY CONCERN

I, Dr. (Mrs.) Jayalekshmi M Nair, Principal (HOD), Vivekanand Education Society's Institute of Technology, do hereby state that the documents uploaded on NAAC portal are duly signed by Principal (HOD).

The additional documents uploaded on Institute's website (<https://vesit.ves.ac.in/>) are also authentic and does not need any extra validation.

  
**Dr. (Mrs.) Jayalekshmi M Nair**  
Principal  
Vivekanand Education Society's Institute of Technology  
Hashu Advani Memorial Complex,  
Collector's Colony  
Chembur, Mumbai, Maharashtra 400074





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## Chip to Startup (C2S) Grant

VESIT has received Grant-in-Aid of Rs 82.59 Lakhs from Ministry of Electronics and Information Technology (MeitY), Govt. of India for the Academia-Industry Collaborative project "Development of ASICs or SoCs for programmable gain amplifier and reconfigurable ADCs for wide range of applications" under "Chips to Startup (C2S) Category - I.



**Chips to Startup: Grant-in-Aid of Rs 82.59 Lakhs from MeitY**



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## C2S sanction letter Grant-in-Aid of Rs 82.59 Lakhs

Category- I(12(A))  
CR&SD-Model-I

EE-9/2/2021-R&D-E  
Government of India

Ministry of Electronics & Information Technology  
(Microelectronics Development Division)

New Delhi-110003  
Dated: 21.07.2023

To

The Pay & Accounts Officer  
Pay & Accounts Office  
Ministry of Electronics and Information Technology  
Electronics Niketan,  
New Delhi -110003.

**Subject:** Release of Grants-in-aids for the implementations of the Project "Development of ASICs or SoCs for programmable gain amplifier and reconfigurable ADCs for wide range of applications" to be implemented by V. E. S. Institute of Technology, Mumbai and M/s Panache Digilife Limited, Thane under Chips to Startup (C2S) Programme.

**Implementing Agency (Lead):** V. E. S. Institute of Technology, Mumbai

**Administrative Approval:** EE-9/2/2021-R&D-E dated 18.05.2023.

Sir,

I am directed to refer to Administrative Approval dated 28.12.2021 and subsequent letter dated 21.02.2022 & 18.05.2023 regarding implementation of Chips to Startups (C2S) Programme by participating organizations across the country including Academic Institutions, R&D organizations Startups and MSMEs at a total estimated cost of Rs.250 Cr over a period of Five years. I am directed to refer to Administrative Approval dated 18.05.2023 for the implementation of above project under Category I (S.No-12) of Programme "Chips to Startup (C2S) at a total estimated cost of Rs. 82.59Lakh (Rupees Eighty Two Lakh Fifty Nine Thousandonly)Rs. 74.32 Lakh from Ministry of Electronics and Information Technologyas Grant-in-Aid and Rs. 8.27 Lakh from M/s Panache Digilife Limited, Thane. The duration of the project is 3 years.

2. I am also directed to convey the sanction of the President of India to the release of **Rs. 3,07,912 Lakhs (Rupees Three Lakh Seven Thousand Nine Hundred Twelve Only)** as part payment of first installment of Grants-in-Aid to V. E. S. Institute of Technology for the implementation of the above project under Chips to Startup (C2S) Programme for the financial year 2023-24(in terms of MoF's OM No.3/(41)/PFMS/2022 date 20/04/2023 the release is not more than 25 % of the amount earmarked for M/S in one installment in FY 2023-2024).

3. The Grants-in-aid will be regulated in accordance with the provisions contained in the Terms and Conditions of Ministry of Electronics and Information Technology and Administrative Approval of even number dated 18.05.2023. The Grants-in-aid is also subject to the Chapter 9 of the General Financial Rules, 2017, as amended from time to time, read with the Government of India's decisions incorporated there-under, and any other guidelines which may be issued in this regard and in particular to the following conditions:

- (i) All relevant information and documents/certificates as required under GFR 230(1) have been received.



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- (ii) The pattern of assistance of Rules governing such Grants-in-aid has received the approval of the Ministry of Finance, as required under Government of India Decision No.(1) under DFPR – Rule 20.
  - (iii) Terms and Conditions of the service of the employees employed in V. E. S. Institute of Technology under the project shall not be higher than those applicable to similar categories of employees of Central Government and where it is not in accordance with above, the relaxation of Ministry of Finance will be obtained for such discrepancies as required Rule 230(6) (12) (i) of General Financial Rules, 2017.
  - (iv) Assets acquired wholly or substantially out of Government Grants shall not be disposed off without obtaining the prior approval of the sanctioning authority of Grants-in-aid
  - (v) V. E. S. Institute of Technology make reservations for Scheduled Castes and Scheduled Tribes or OBC in the posts or services under its control on the lines indicated by Government of India.
  - (vi) The accounts of V. E. S. Institute of Technology be audited by C&AG or by any person authorized by him on his behalf in accordance with the provisions laid down in Section 14 of the C&AG (DPC) Act, 1971, as amended from time to time.
  - (vii) The accounts of V. E. S. Institute of Technology be open for inspection by the sanctioning authority and audit, both by the Comptroller & Auditor General of India under the provision of C&AG (DPC) Act, 1971, and internal audit party by the Principal Accounts Office of the Ministry or Department, whenever it is called upon to do so.
  - (viii) V. E. S. Institute of Technology furnish the Utilization Certificate along with its request for release of Grants-in-aid certifying that the funds released to them have been utilized for the purpose for which it was sanctioned.
  - (ix) V. E. S. Institute of Technology furnish their performance-cum-achievement report within the stipulated period to the sanctioning authority.
  - (x) Noted at Serial No.32 in the register of Grant-in-aid for the financial year 2023-24.
  - (xi) V. E. S. Institute of Technology spend Grants-in-aid exclusively in pursuance of the objectives envisaged in Rules/Memorandum of V. E. S. Institute of Technology and for the purpose for which it is being sanctioned.
  - (xii) Grants-in-aid to V. E. S. Institute of Technology is subject to the Economy Instructions issued from time to time by the Ministry of Finance or by the Competent Authority.
  - (xiii) Grants-in-aid shall be utilized within the stipulated period and unspent balance, if any, will be refunded by V. E. S. Institute of Technology to the Government of India.
  - (xiv) V. E. S. Institute of Technology maintain and will present their annual accounts in the standard format, as required under GFR 230 (5) of General Financial Rules, 2017.
  - (xv) All interest of other earnings against GIA or advance (Other than Reimbursement) released should be mandatorily remitted to the Consolidated Fund of India immediately after finalization of the accounts. Such advances should not be allowed to be adjusted against further releases.
  - (xvi) In case of Non-Recurring Grant for specified object:
    - a) V. E. S. Institute of Technology have to spend the amount sanctioned to them within the stipulated period.
    - b) V. E. S. Institute of Technology have to submit the Utilization Certificate in respect of the grants sanctioned under GFR 238(1) in the prescribed form General Financial Rules 2017 read with form GFR 12-A, ibid.
4. V. E. S. Institute of Technology is exempted from the execution of Surety Bond in terms of Ministry of Finance OM No. 14(1)/E/II(A)/64 dated 23.06.1965 and has accepted the terms and conditions for the grants-in-aid.
5. The expenditure involved will be met within the sanctioned Budget Grant of Ministry of Electronics and Information Technology under Demand No. 27, Major Head 2852- (Industries), Sub Major Head: 07 (Telecommunication and Electronics Industries), Minor Head: 102 (Digital India Programme), Detailed Head: 14.00 (Capacity Building and Skill Development), Object



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6. Drawing and Disbursing officer (DDO) of Ministry of Electronics and Information Technology is authorized to prefer the bill to Pay & Account Officer for the release of Rs. 3.07912 Lakhs (Rupees Three Lakh Seven Thousand Nine Hundred Twelve Only) to Central Nodal Agency i.e. National Institute of Electronics and Information Technology (NIELIT) NIELIT (HQ) Delhi will further release grant-in-aid to the implementing agency V. E. S. Institute of Technology Mumbai (Sub agency) as per bank details given below:

Details of Central Nodal Agency, NIELIT Delhi:

Bank's Name	RESERVE BANK OF INDIA
Branch	RESERVE BANK OF INDIA, 6, SANSAD MARG, NEW DELHI - 110001
Account Type	RBI TSA Account
Bank Account No.	10679401010
UDH/RO/CIF Code	737/06/6794

Details of Sub Agency: V. E. S. Institute of Technology Mumbai

Bank's Name	CANARA BANK
Branch	Chembur Camp
Branch Address	Ground Floor, Navbharat Apartments CHS Ltd. Chembur Camp, Chembur, Mumbai
Account Name	VIVEKANAND EDUCATION SOCIETY INSTITUTE OF TECHNOLOGY
Bank Account No.	110131674944
IFSC Code	CNRB0000170
Account Type	Saving Account

7. "It is informed that the Grantee Institute has no UC as due for rendition under the Rules in r/o all Schemes/Programmes/Projects of the Ministry of Electronics and Information Technology" as the present release is the first release to the V. E. S. Institute of Technology, Mumbai.

8. This issues with the approval of Secretary, MeitY vide computer No. 3080449 dated 03.05.2023 and concurrence of JS&FA, Ministry of Electronics & Information Technology vide computer No. 3080449 dated 03.05.2023.

Yours faithfully,

*Meenakshi Kumar*  
21/7/2023

(Meenakshi Kumar)

Under Secretary to the Govt. of India

Copy to:




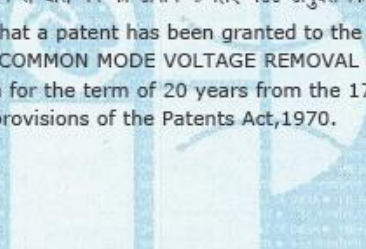

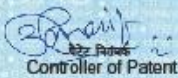
1. The Office of the Principal Director of Audit, Finance and Communication, Civil Lines, Near old Secretariat, Sharnath Marg, Delhi-110054
2. Prof. Nilima Warke, Chief Investigator, V.E.S. Institute of Technology, Hashu Advani Memorial Complex, collector's Colony, Chembur, Maharashtra 400074
3. Shri. Amit Rambhia., Chief Investigator, Panache Digilife Limited, Bldg. A3, 102-108 & 201-208, Babosa Industrial Park, Mumbai-Nashik Highway NH3, Saravali Village, Bhiwandi, Thane, Maharashtra -421302
4. DG(NIELIT)/CFO(NIELIT)
5. Sh. N.K. Singh, Sr. Audit Officer, RAO, MeitY
6. GC(SV)/GC(AKP)/Sci. 'E'(NG)/Sci. 'D'(HG)/DS(DKS), MeitY
7. Finance Division/HRD/D&D Section, MeitY



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## PATENT GRANTED

 <b>INTELLECTUAL PROPERTY INDIA</b> PATENTS   DESIGNS   TRADE MARKS GEOGRAPHICAL INDICATIONS	 भारत सरकार GOVERNMENT OF INDIA पेटेंट कार्यालय THE PATENT OFFICE पेटेंट प्रमाणपत्र PATENT CERTIFICATE (Rule 74 of The Patents Rules)	क्रमांक : 022123810 SL No : 
पेटेंट सं. / Patent No.	:	428537
आवेदन सं. / Application No.	:	201921042142
फाइल करने की तारीख / Date of Filing	:	17/10/2019
पेटेंटी / Patentee	:	1.Mrs. Nilima Warke 2.Dr. J. M. Nair 3.Dr. P. P. Vaidya
<p>प्रमाणित किया जाता है कि पेटेंटी को, उपरोक्त आवेदन में बयाप्रकटित DEVICE FOR COMMON MODE VOLTAGE REMOVAL नामक आविष्कार के लिए, पेटेंट अधिनियम, 1970 के उपबंधों के अनुसार आज तारीख अक्टूबर 2019 के सत्रहवें दिन से बीस वर्ष की अवधि के लिए पेटेंट अनुदत्त किया गया है।</p> <p>It is hereby certified that a patent has been granted to the patentee for an invention entitled DEVICE FOR COMMON MODE VOLTAGE REMOVAL as disclosed in the above mentioned application for the term of 20 years from the 17<sup>th</sup> day of October 2019 in accordance with the provisions of the Patents Act,1970.</p>		
 <b>INTELLECTUAL PROPERTY INDIA</b> PATENTS   DESIGNS   TRADE MARKS GEOGRAPHICAL INDICATIONS		
		 Controller of Patent
अनुदान की तारीख : Date of Grant :	:	10/04/2023
<p>टिप्पणी - इस पेटेंट के नवीकरण के लिए फीस, यदि इसे बनाए रखा जाना है, अक्टूबर 2021 के सत्रहवें दिन को और उसके पचास फ़ीस नंबर से उड़ी दिन देय होगी। Note. - The fees for renewal of this patent, if it is to be maintained will fall / has fallen due on 17<sup>th</sup> day of October 2021 and on the same day in every year thereafter.</p>		



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## Certificate of IIC Establishment

